

Master Nanosciences et nanotechnologies

Hybrid Electronics 1: Advanced memories

Responsable	Descriptions	Informations
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LANGUE(S) D'ENSEIGNEMENT

Anglais

CONTENU

This course offers a detailed study of the main families of memory devices, present in current technological objects. Once the economic / industrial context related to this type of components is presented, a state of the art will identify the main memory devices. Their operating mode will then be detailed, with the description of the underlying physical phenomena.

Topics :

- General notes on memory devices : the economic / industrial context, main families of memory devices, main characteristics
- Volatil memorie
- Traditional non-volatile memories : Flash, EEPROM, ...
- Advanced non-volatile memories: resistive, ferroelectric, magnetic memories, with organic components
- Study of a specific memory device (to be choosen)

PRÉ-REQUIS OBLIGATOIRES

Physics of nanodevices

VOLUME HORAIRE

- Volume total: 18 heures
- Cours magistraux: 12 heures
- Travaux dirigés: 6 heures

CODES APOGÉE

- SNNCU23J [ELP]

M3C

Aucune donnée M3C trouvée

POUR PLUS D'INFORMATIONS

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Dernière modification le 29/06/2023